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# INTEL Pine Trail Platform

## N10C

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Version :0.1

### Notes:

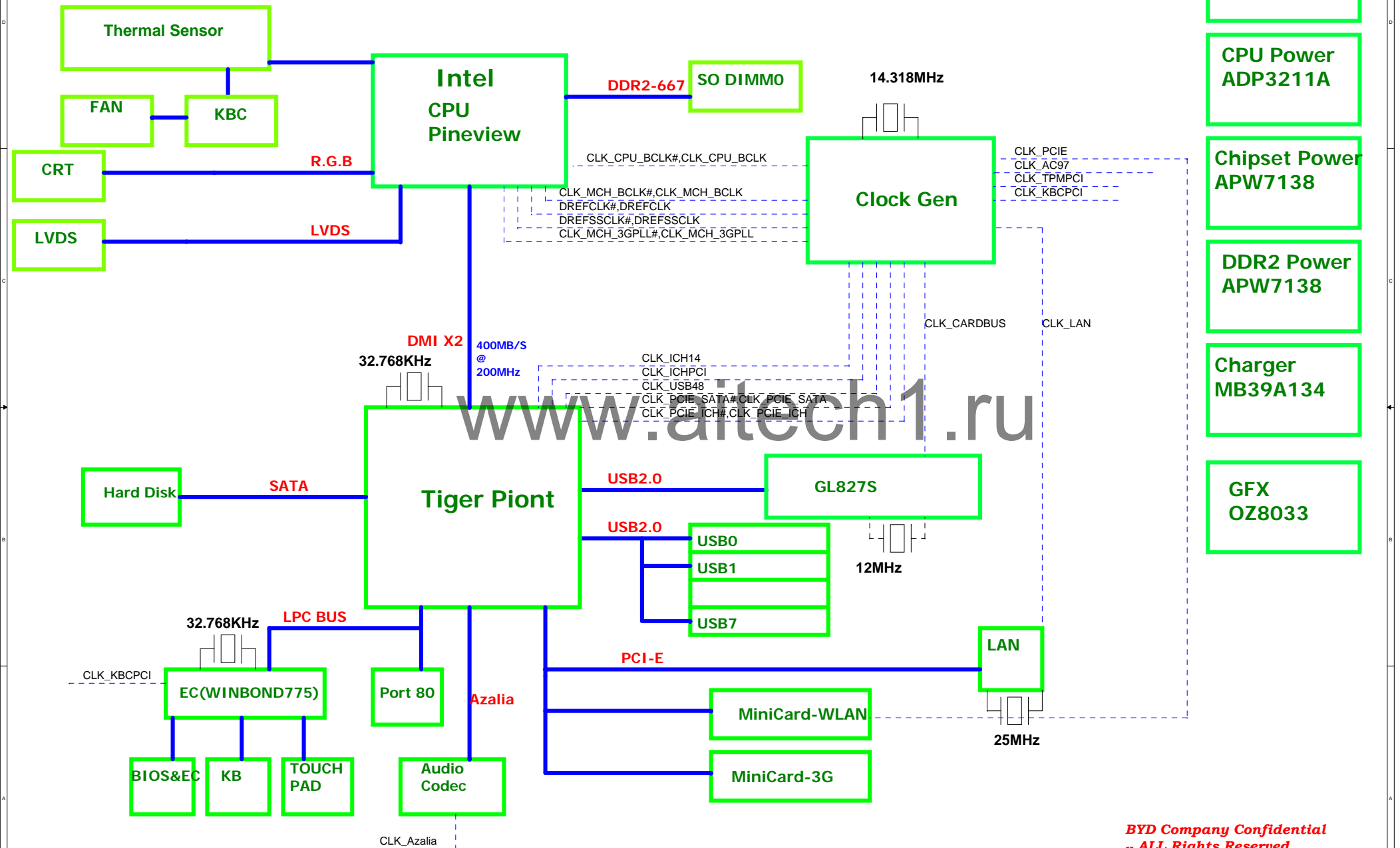
Part Value Prefix : "POP=NA" means no pop

Net Value postfix : "#" means Low Active

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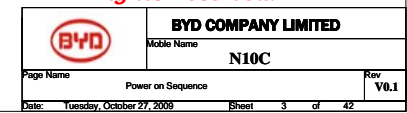
		<b>BYD COMPANY LIMITED</b>	
Page Name		Mobile Name	
Cover sheet		N10C	
Date: Tuesday, October 27, 2009		Sheet 1 of 42	Rev V0.1

# BLOCK DIAGRAM

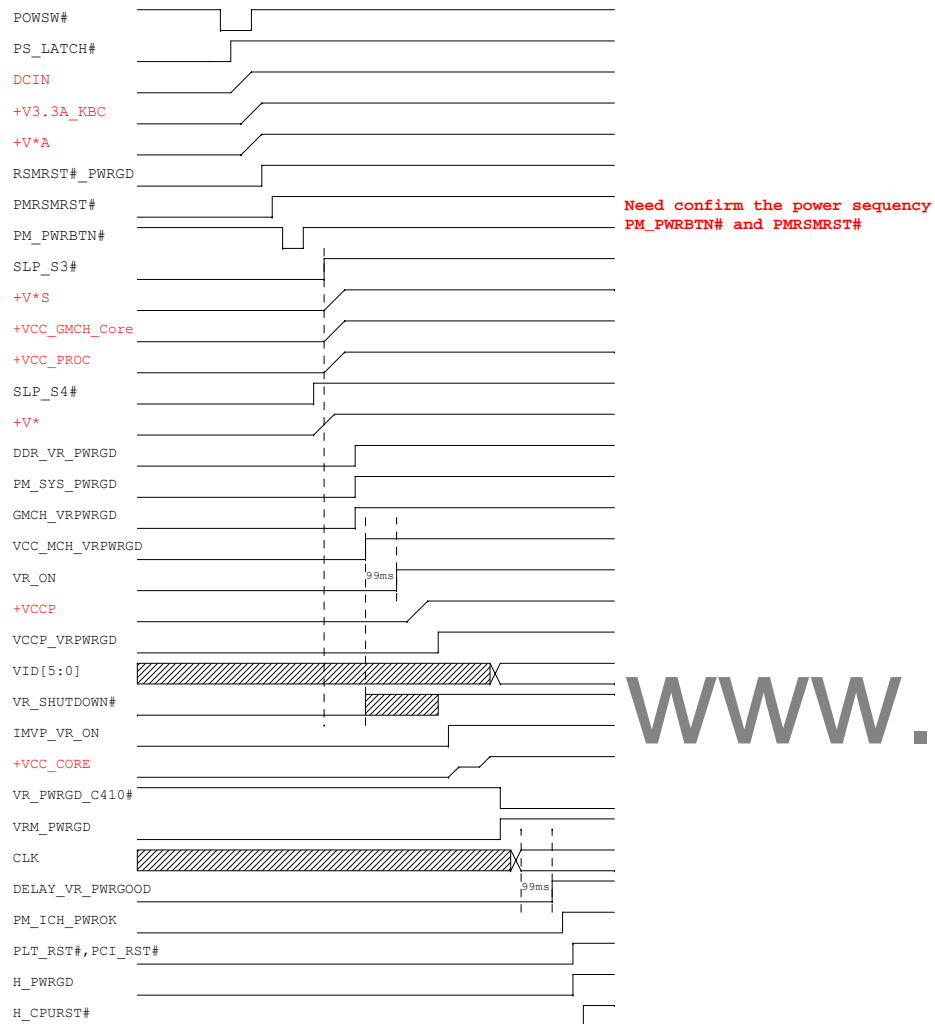


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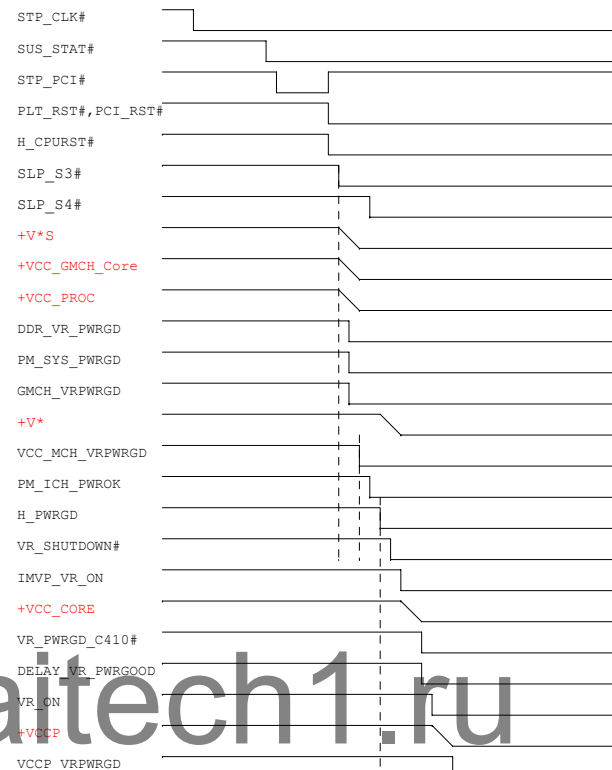
ACIN  
Circuit  
PG:30



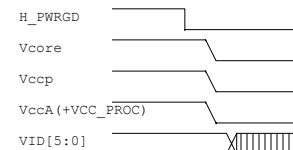
# Power On Sequence



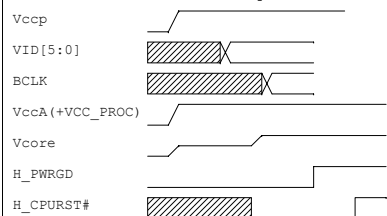
# Power Down Sequence



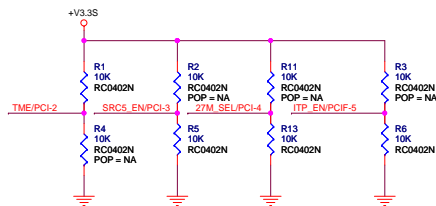
## CPU Power Down Sequence Intel request:



## Intel request: CPU Power on Sequence

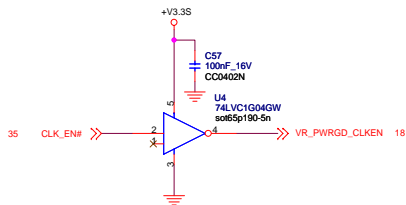
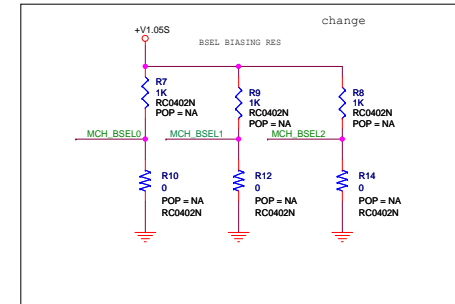
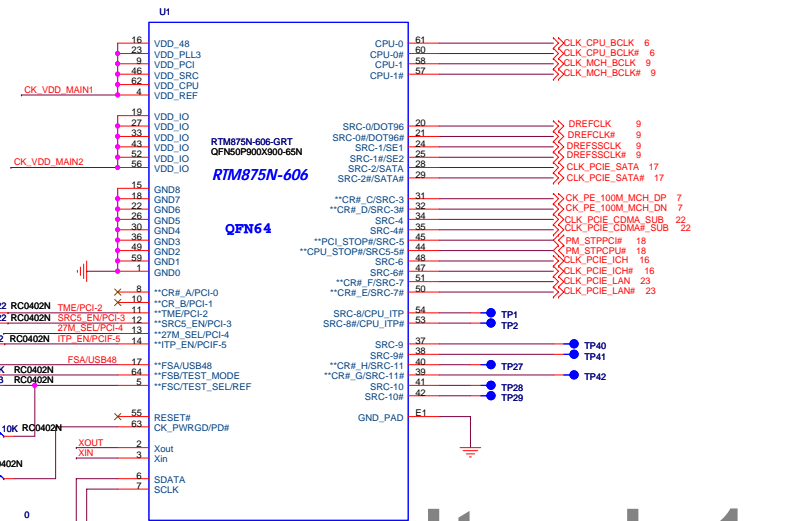


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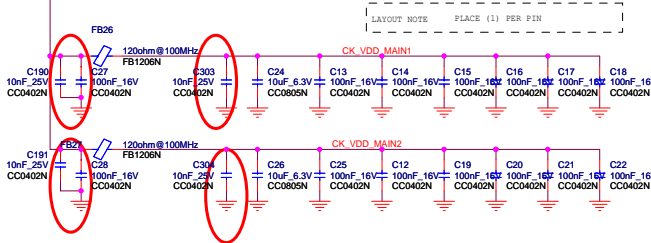


## 12-15 更换CLOCK芯片, 将RTM875T-606 换为RTM875N-606-VD-GR

	PULL HIGH	PULL LOW
TME/PCI-2	NO OVERCLOCKING	NORMAL RUN
SRC5_EN/PCI-3	Pin 45/44 is SRC5	Pin 45/44 is PCI_STOP/CPU_STOP
27M_SEL/PCI-4	Pin 20/21 is SRC-0 Pin 24/25 is 27/278S	Pin 20/21 is DOT96 Pin 24/25 is SRC-1
ITP_EN/PCIF-5	Pin 54/53 is CPUITP	Pin 54/53 is SRC8



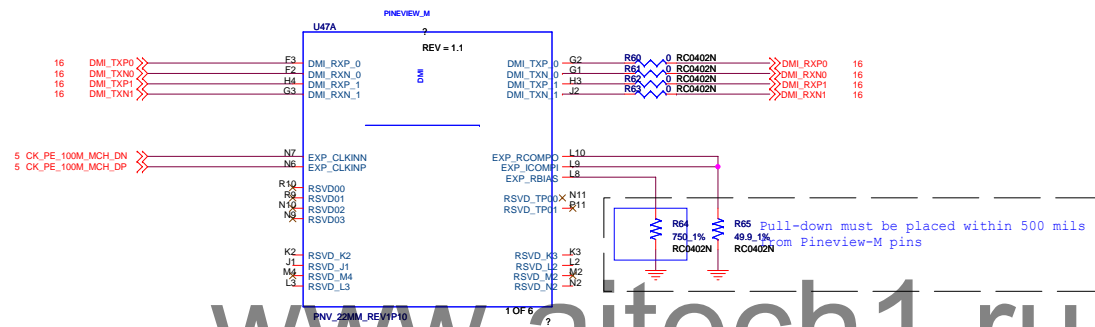
电容靠近磁珠



FS <sub>C</sub>	FS <sub>B</sub>	FS <sub>A</sub>	CPU	SRC	PCI	DOT	REF	USB
Bit2	Bit1	Bit0	(MHz)	(MHz)	(MHz)	(MHz)	(MHz)	(MHz)
0	0	0	266.66	100.00	33.33	96.00	14.318	48.00
0	0	1	133.33	100.00	33.33	96.00	14.318	48.00
0	1	0	200.00	100.00	33.33	96.00	14.318	48.00
0	1	1	166.66	100.00	33.33	96.00	14.318	48.00
1	0	0	333.33	100.00	33.33	96.00	14.318	48.00
1	0	1	100.00	100.00	33.33	96.00	14.318	48.00
1	1	0	400.00	100.00	33.33	96.00	14.318	48.00
1	1	1	200.00	100.00	33.33	96.00	14.318	48.00

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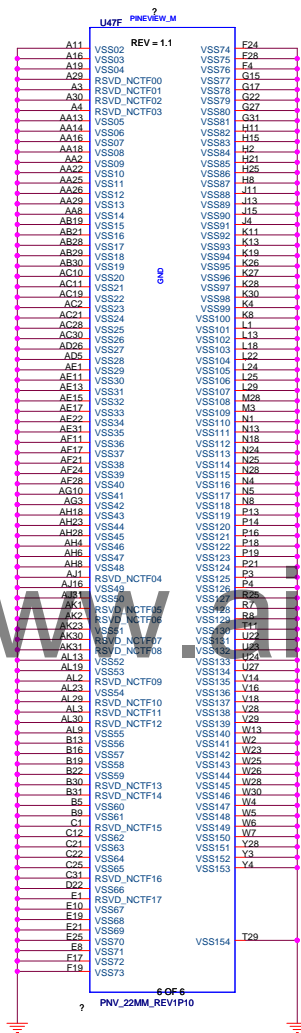
		<b>BYD COMPANY LIMITED</b>	
		Mobile Name <b>N10C</b>	
Page Name <b>CPU2(DMI)</b>	Date: <b>Tuesday, October 27, 2009</b>		Rev <b>V0.1</b>
Sheet <b>7</b> of <b>42</b>			












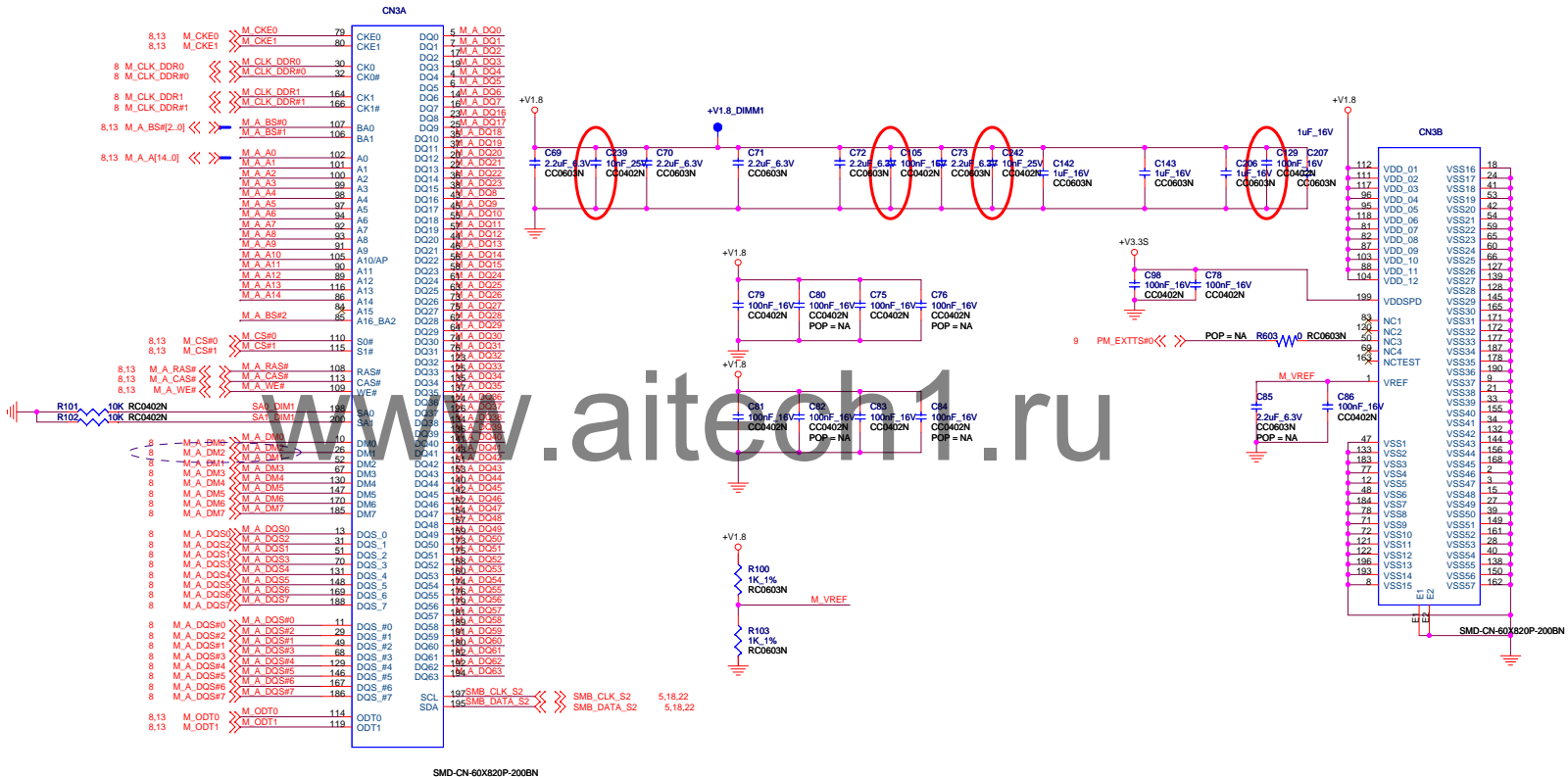
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		BYD COMPANY LIMITED	
		Model Name	
		N10C	
Page Name	CPU6(GND)	Rev	V0.1
Date:	Tuesday, October 27, 2009	Sheet	11 of 42

```

M_A_DQ[7..0] 8
M_A_DQ[15..8] 8
M_A_DQ[23..16] 8
M_A_DQ[31..24] 8
M_A_DQ[39..32] 8
M_A_DQ[47..40] 8
M_A_DQ[55..48] 8
M_A_DQ[63..56] 8

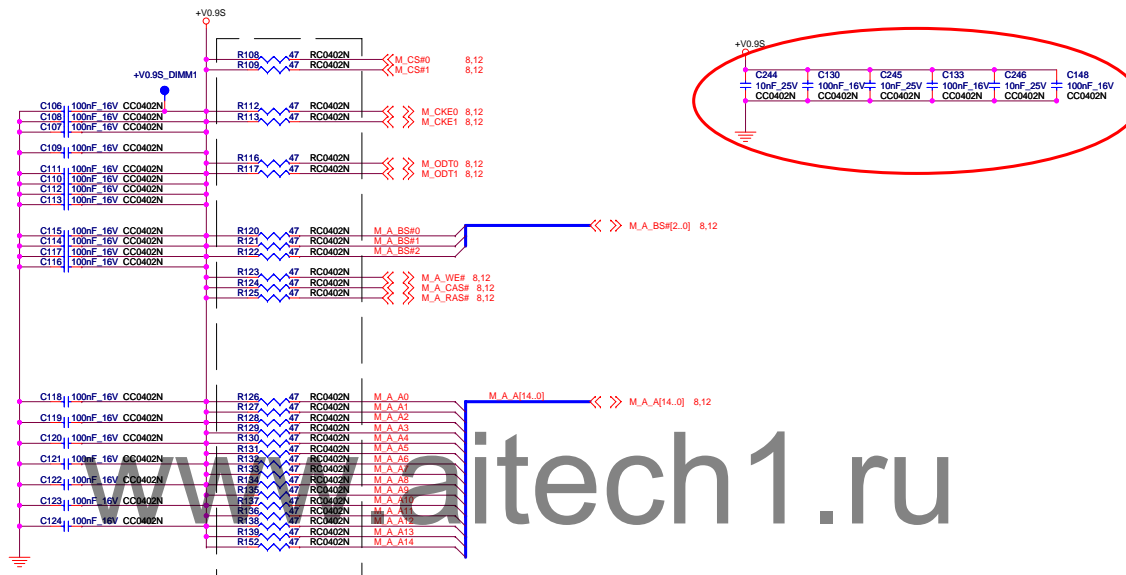
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	<b>BYD COMPANY LIMITED</b>		
	Mobile Name <b>N10C</b>		
Page Name <b>DDR2_SODIMMO</b>	Rev <b>V0.1</b>		
Date: <b>Tuesday, October 27, 2009</b>	Sheet <b>12</b> of <b>42</b>		

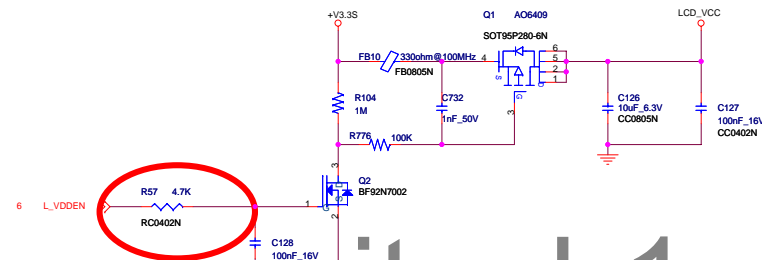
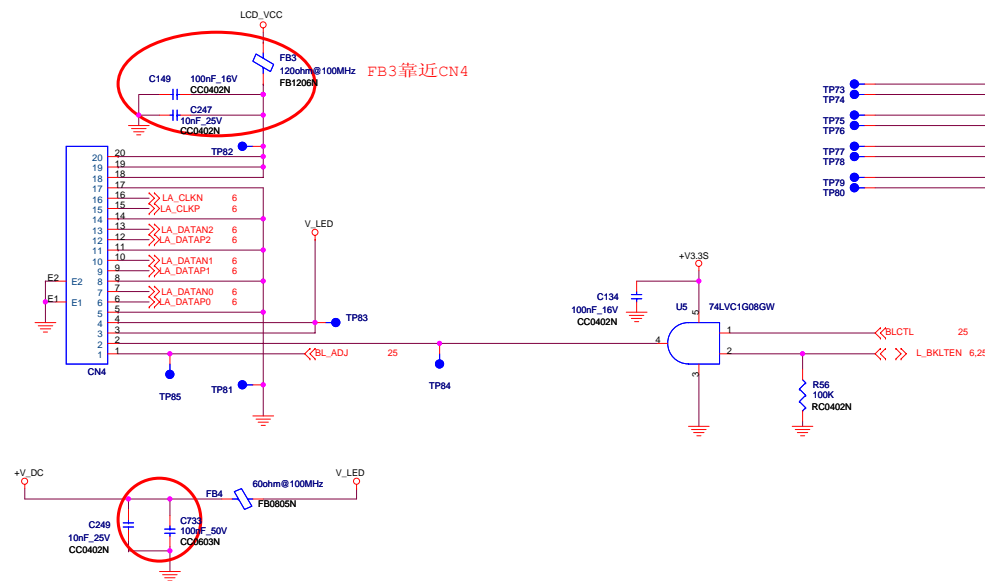
# DDR2 Termination

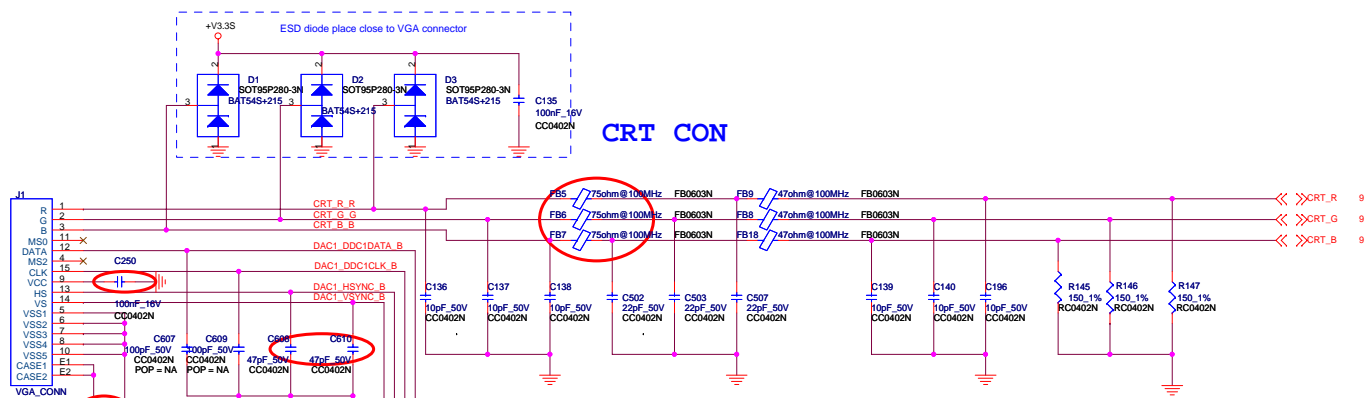
Layout note:  
Place one cap  
close to every 2  
pullup resistors  
terminated to +0.9S



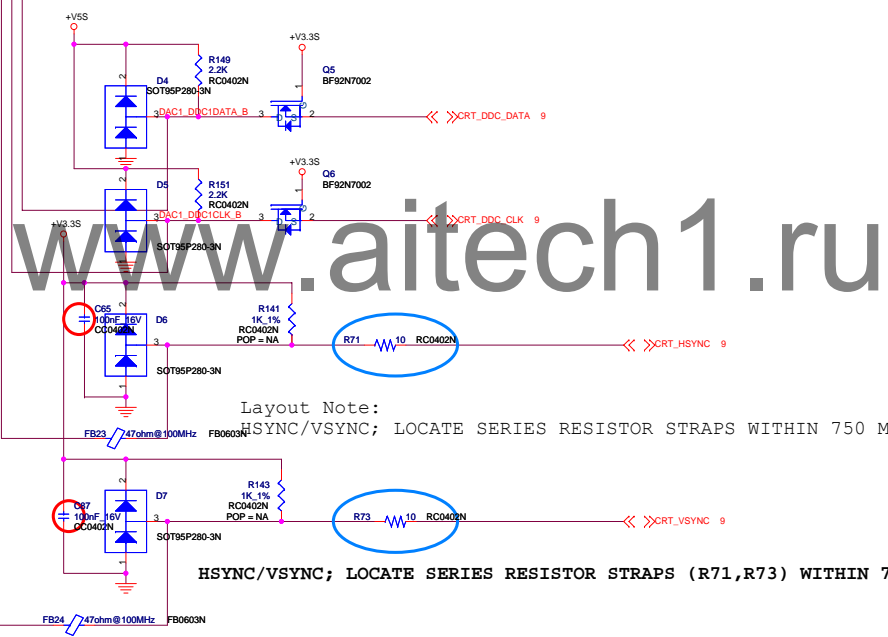
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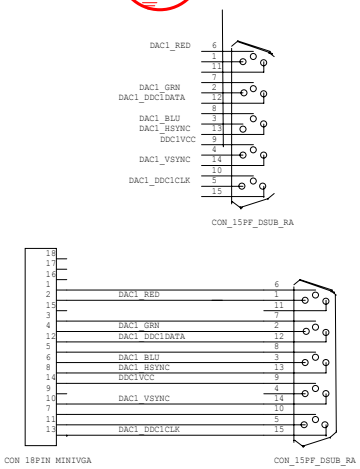


### BUFFER AND LEVEL SHIFT LOGIC



Layout Note:  
HSYNC/VSNC; LOCATE SERIES RESISTOR STRAPS WITHIN 750 MILS OF MCH

HSYNC/VSNC; LOCATE SERIES RESISTOR STRAPS (R71,R73) WITHIN 750 MILS OF MCH



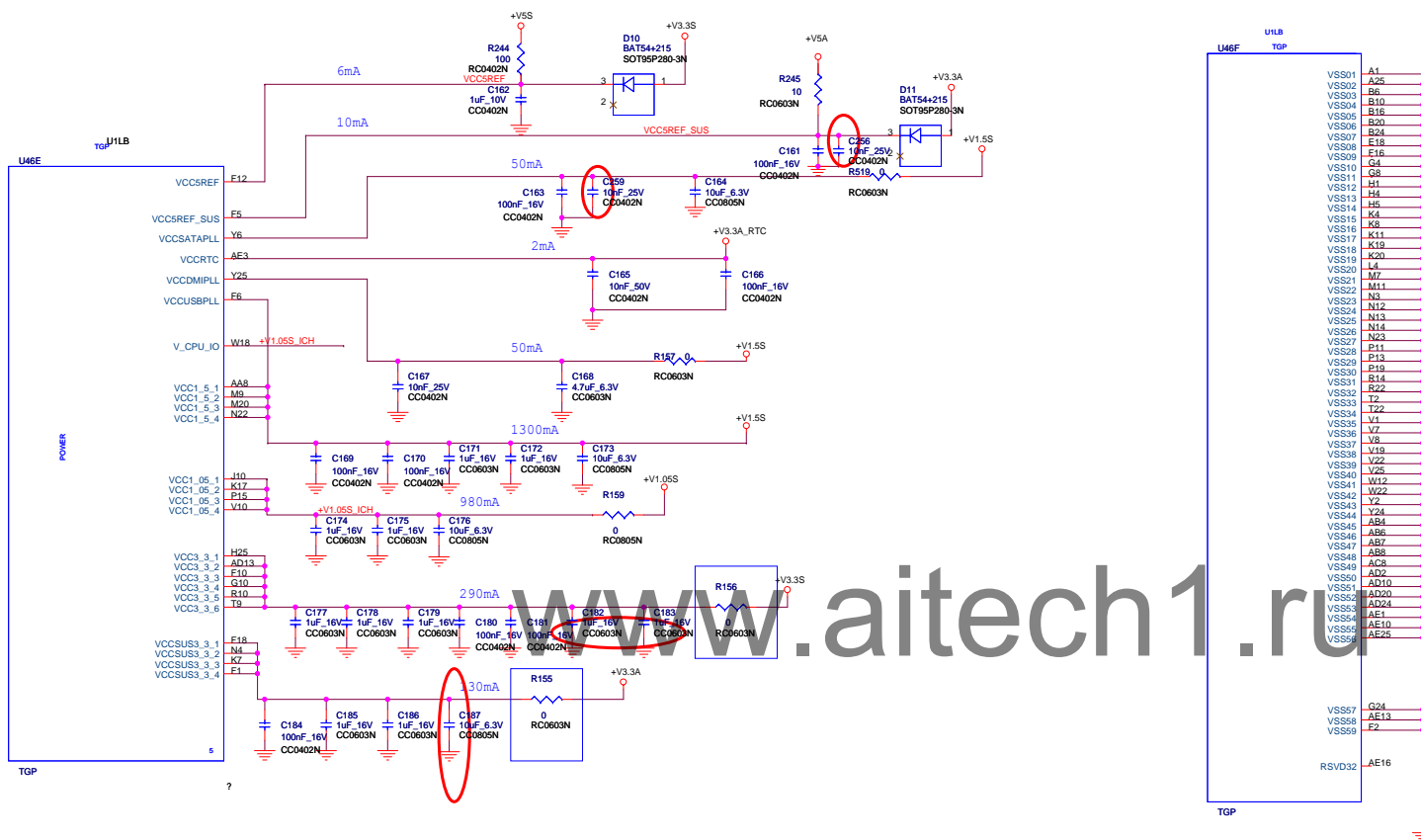
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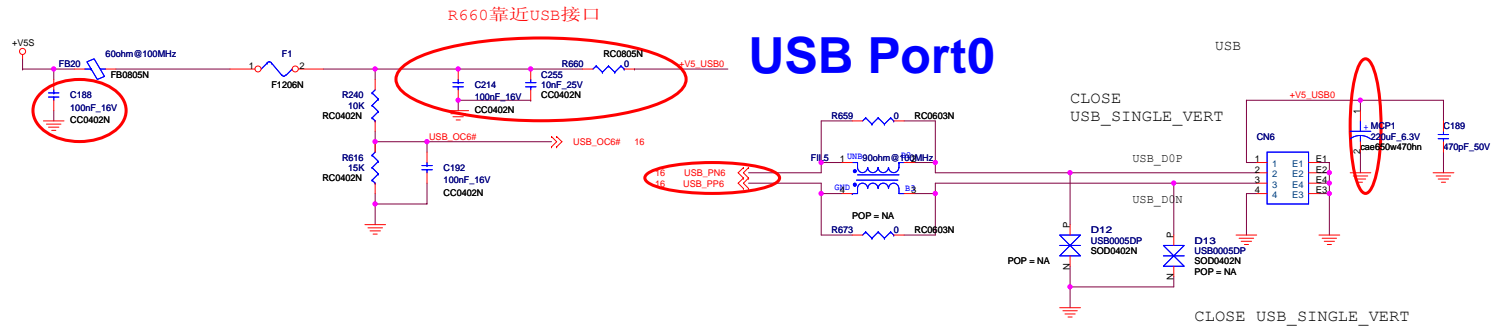




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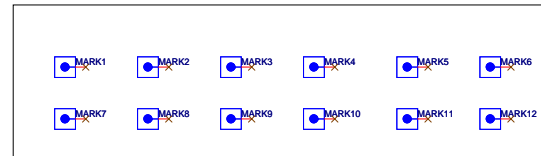
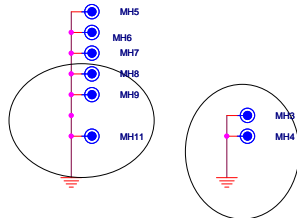
# USB

## USB Port0




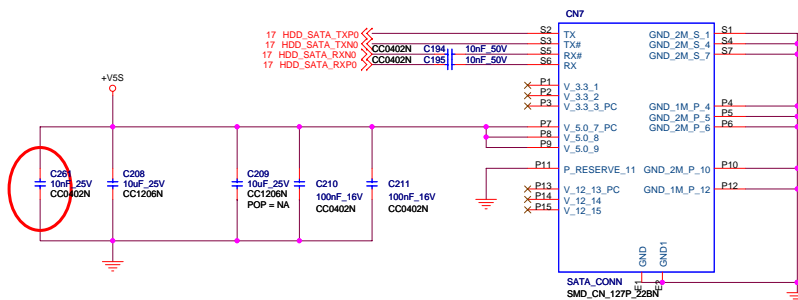
# LED

### Bottom LED



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	<b>BYD COMPANY LIMITED</b>		
	Mobile Name <b>N10C</b>		
Page Name <b>USB_PORT&amp;LED</b>	Rev <b>V0.1</b>		
Date: <b>Tuesday, October 27, 2009</b>	Sheet <b>20</b>	of <b>42</b>	

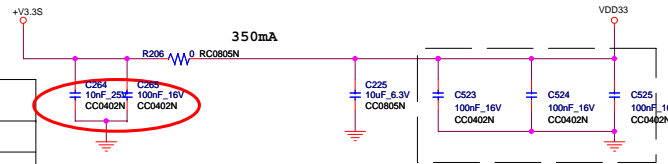


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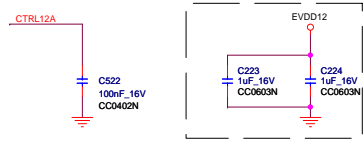


# Power domain chart

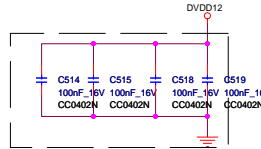
	RTL8103EL	
AVDD33	3.3V	
AVDD18	1.2V	
EVDD12	1.2V	
DVDD12	1.2V	



Layout Note:  
Place close to VDD33 PINS.  
(PIN1, PIN29, PIN37)

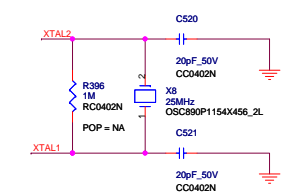
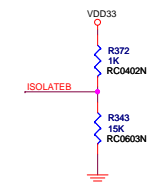
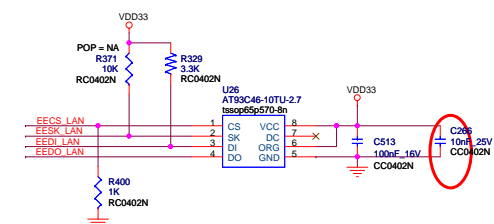
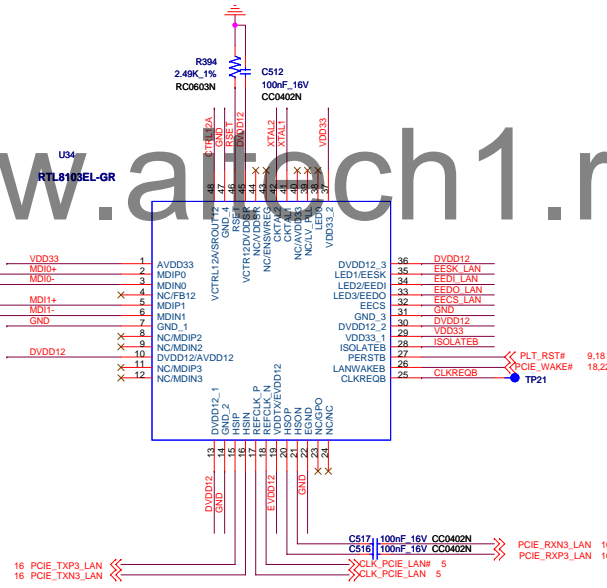


Layout Note:  
Place close to EVDD12 PINS.  
(PIN19)



Layout Note:  
Place close to DVDD12 PINS.  
(PIN10, PIN13, PIN30, PIN36)

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<<Attention>>  
Surges of PVDD >7V duration 0.1ms when class D amplifier is working may damage the amplifier, 10uF tantalum capacitors are required at PVDD1 and PVDD2 to suppress the surge.

根据实际测试结果进行调整

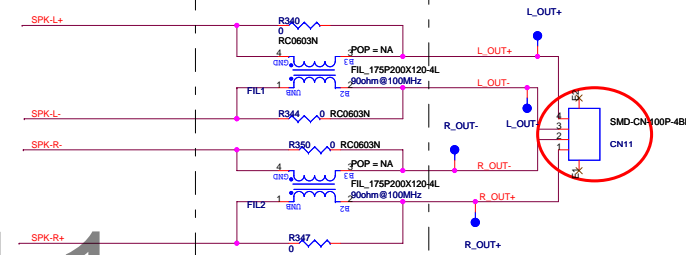
电容靠近ALC269处放置

<<Attention>>  
If you mount the LC filter(L1-L4;C4/C11;C2/C7/C10/C13),Please let them together and close to codec. If the PCB trace and Speaker wire length is less than 20cm, you don't need the LC filter(L1-L4;C4/C11).  
To eliminate the EMI,if L1,L2,L3,L4 are replaced by 0 ohm/1.6A resistor(please don't use general bead, because it may influence the THD+N quality),and C4,C11 should be NC.And,please make the trace length/ Speaker wire length of SPKL+/L-/R-/R- be the same as possible as you can.  
C2/C7/C10/C13 are reserved for EMI fine-tune; For EMI issue, please also refer our ALC269 Layout guide document

Demodulation Filter  
Placement near Audio Codec

Close to the ALC269

Close to the CONN



参考原理图使用2.2uF ~ 4.7uF

Internal MIC

Tied at one point only under the ALC269 or near the ALC269

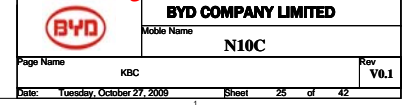
Configuration for ALC269 ( Mobile system only )  
Internal Speaker + Internal Mic + PCBEEP +( SPDIF Out )  
HeadPhone + External Mic + SPDIF Out

Layout注意: 每组电源与地之间的电容需要放置在该组对应的两个PIN之间

<<Attention>>  
For power\_on/off de-pop circuit and system booting warning signal: Please System BIOS Engineer Note :  
1. If you want the system make warning signal after power on , please let EC\_MUTE# High first.  
2.When you want to exit your Bios Programming Code, please let the EC\_MUTE# Low.(The programming is different from before . )

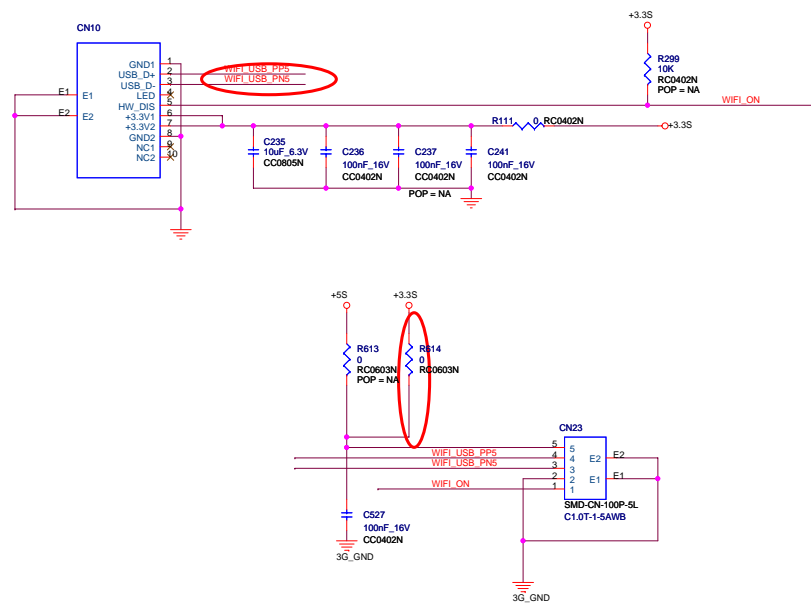
PD#0V : Power down Class D SPK amplifier  
PD#3.3V : Power up Class D SPK amplifier



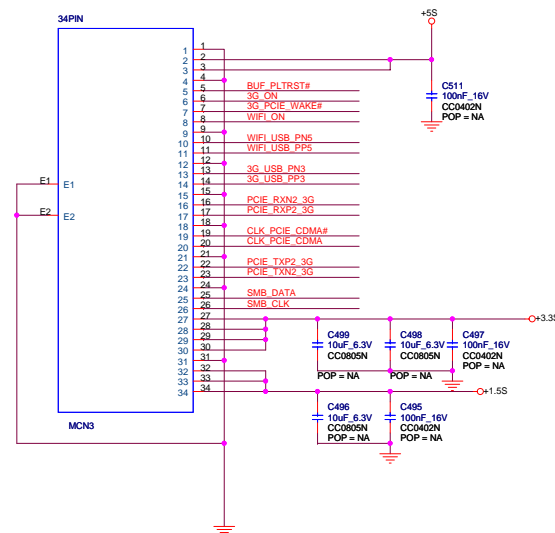




# WIFI

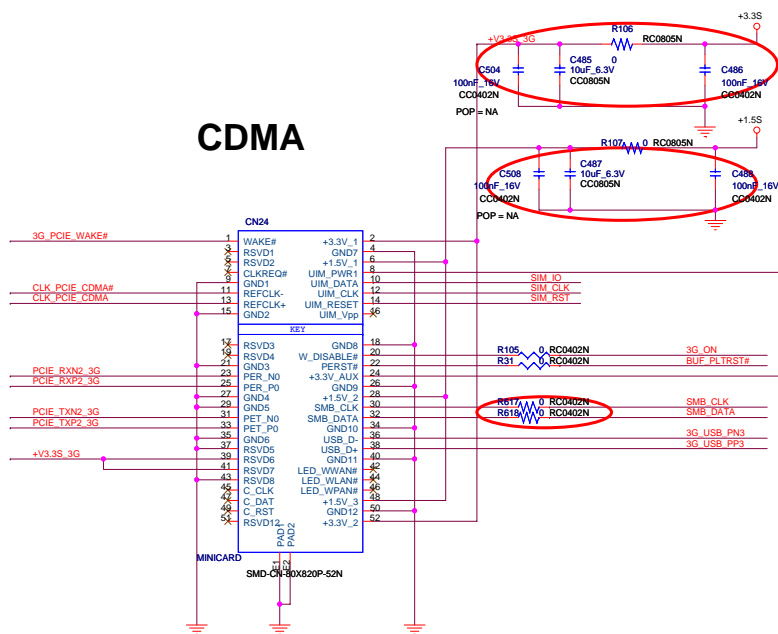


# MiniCard-3G

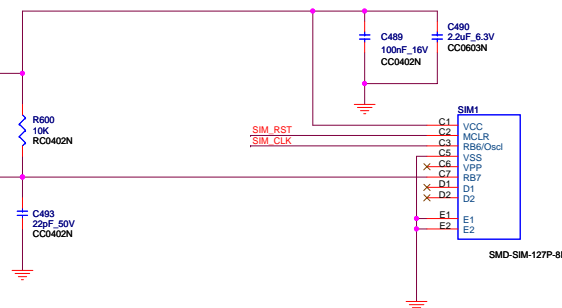


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# CDMA



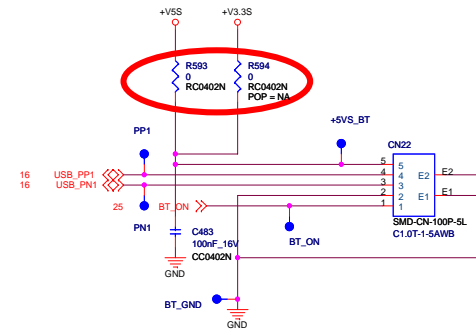
# SIM CARD



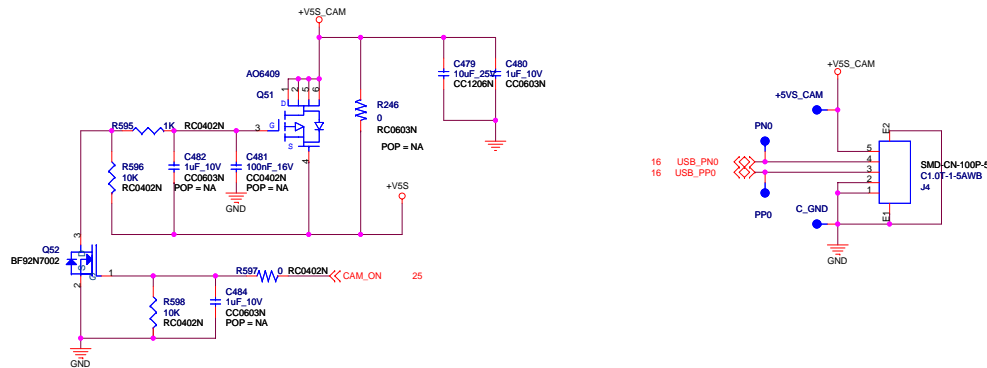
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BYD COMPANY LIMITED	
Mobile Name	N10C
Page Name	3G_MINI_PCIE
Date: Tuesday, October 27, 2009	Sheet 27 of 42
Rev	V0.1

## Bluetooth CONN

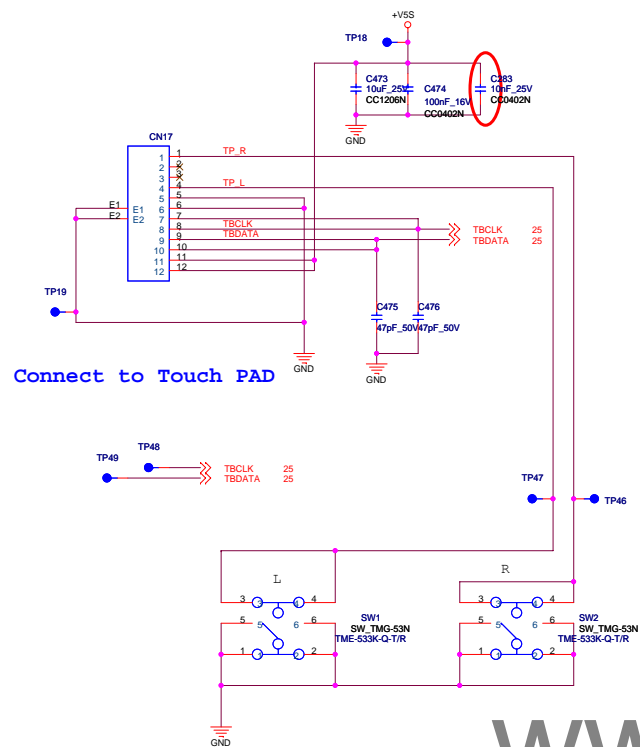


## Camera CONN

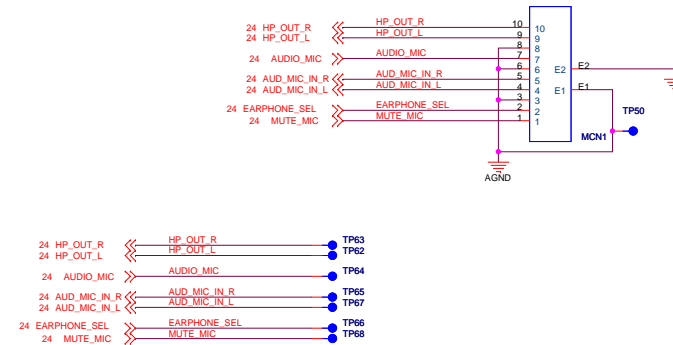


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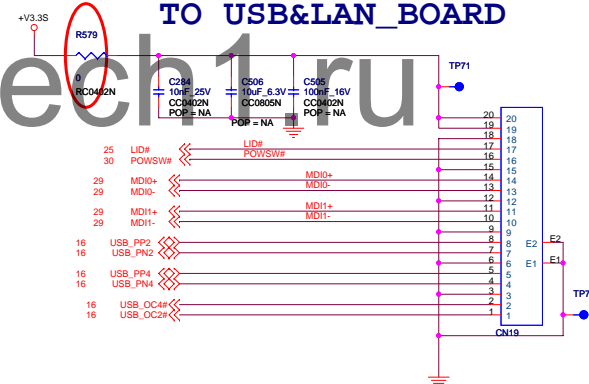
	<b>BYD COMPANY LIMITED</b>	
	Mobile Name	<b>N10C</b>
Page Name	40 Option device:BT/Camera/TCM	
Date: Tuesday, October 27, 2009	Sheet 28 of 42	Rev <b>V0.1</b>



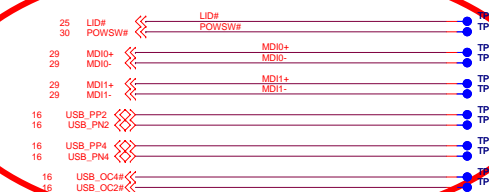
## TO Audio\_BOARD



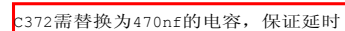
## TO USB&LAN\_BOARD




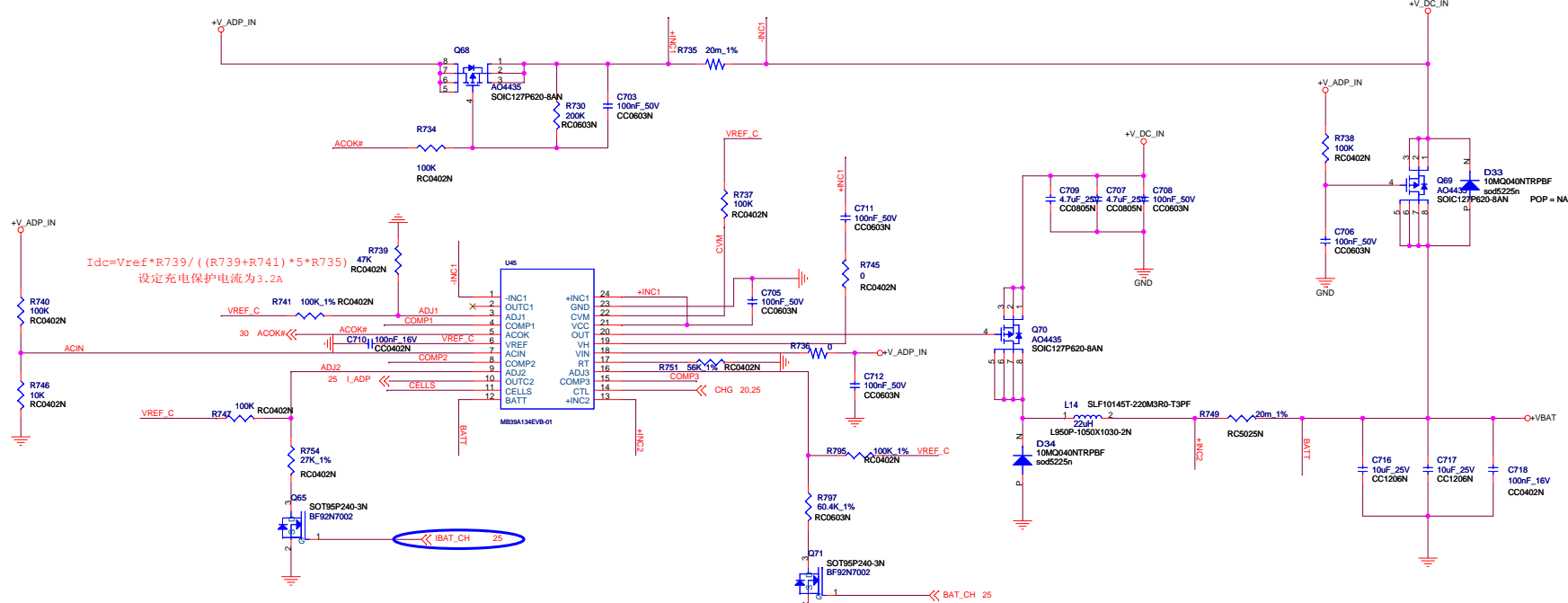
## TO SW\_BOARD



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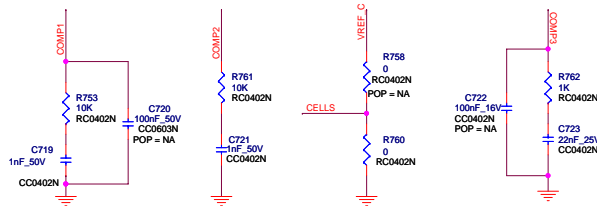


		<b>BYD COMPANY LIMITED</b>	
Mobile Name		<b>N10C</b>	
Page Name	ACIN & 5V_LDO		Rev <b>V0.</b>
Date:	Tuesday, October 27, 2009	Sheet	30 of 42



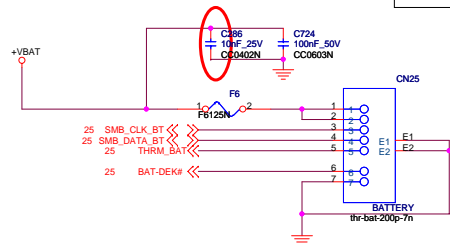
如果Q65导通  
 $I_{bat} = 1.7A = 2(V_{adj2} - 0.075)$   
 $V_{adj2} = V_{REF} * (R754 / (R747 + R754))$   
 将R806设定为24K, 充电电流为: 1.52A  
 如果Q65不导通  
 充电电流为2.85A

$V_{bat} = 3.75 = 2V_{adj3}$   
 $V_{adj3} = V_{REF} * (R797 / (R795 + R797))$   
 将R797设定为60.4K, 充电电压为: 3.74V



## Battery Connector

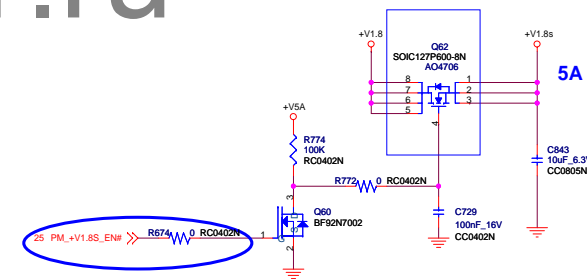
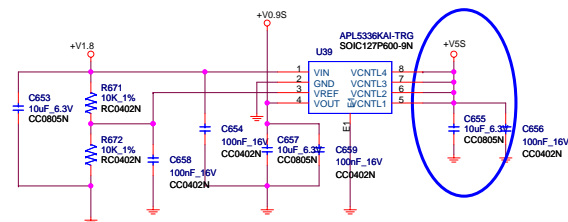
Place R758=0 for 4cell operation  
 Place R760=0 for 3cell operation  
 Open R758 & R760 for 2cell operation




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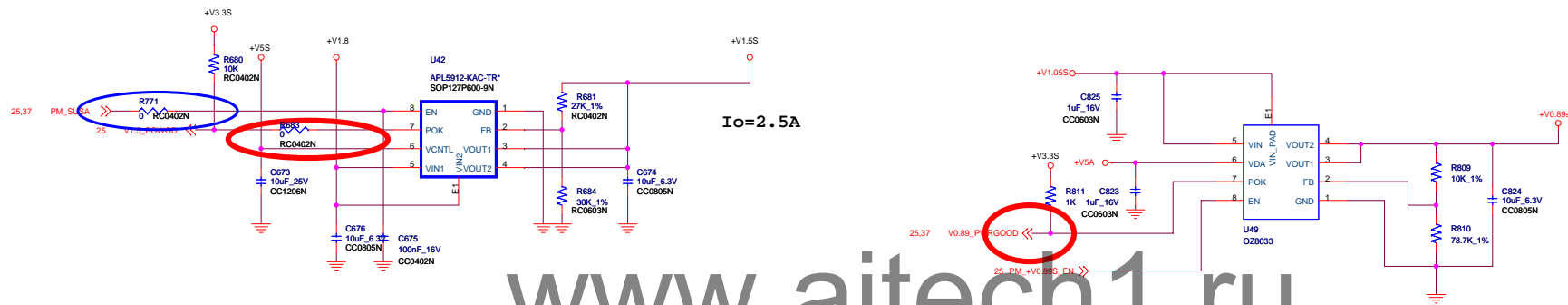





$$V_{set} = V_{ref} * R4 / (R3 + R4)$$


	<b>BYD COMPANY LIMITED</b>		
	Mobile Name <b>N10C</b>		
Page Name <b>PWR_V1.1.8 &amp; V1.05S</b>	Rev <b>V0.1</b>		
Date: <b>Tuesday, October 27, 2009</b>	Sheet <b>33</b>	of <b>42</b>	

## 1.5S Power control

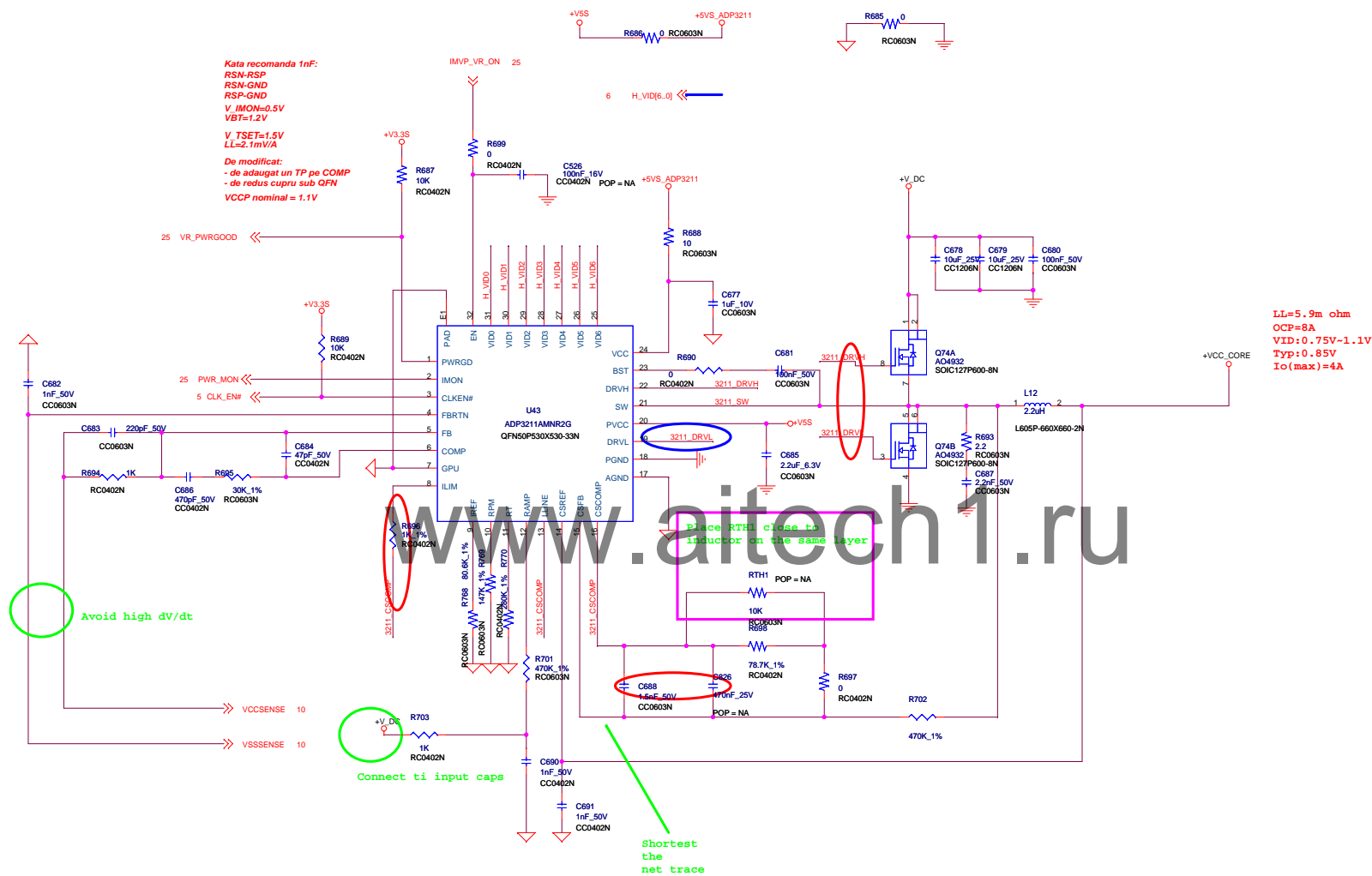


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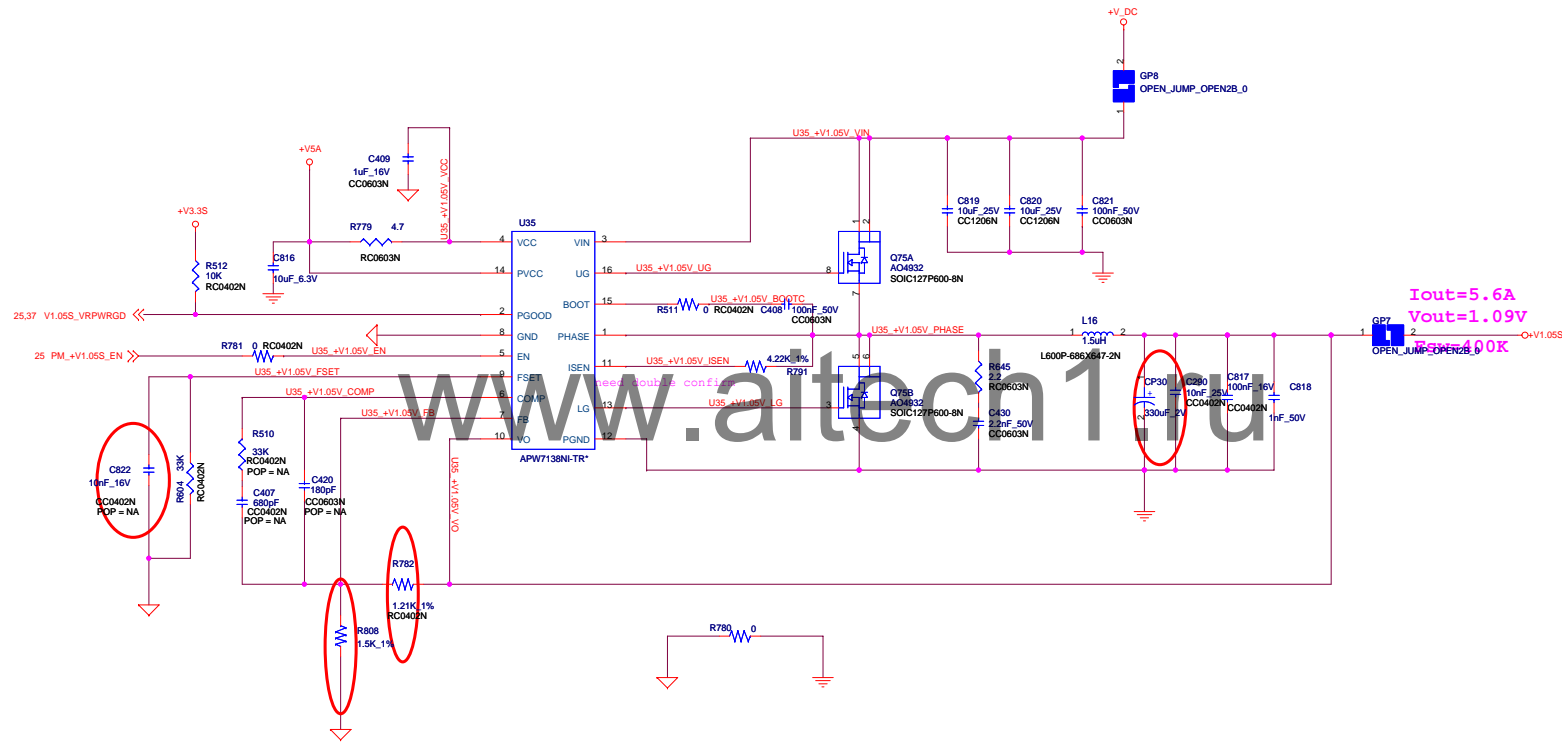
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	<b>BYD COMPANY LIMITED</b> Mobile Name <b>N10C</b>
Page Name PWR_+V1.55 & +V0.89S	Rev <b>V0.1</b>


Date: Tuesday, October 27, 2009 Sheet 34 of 42

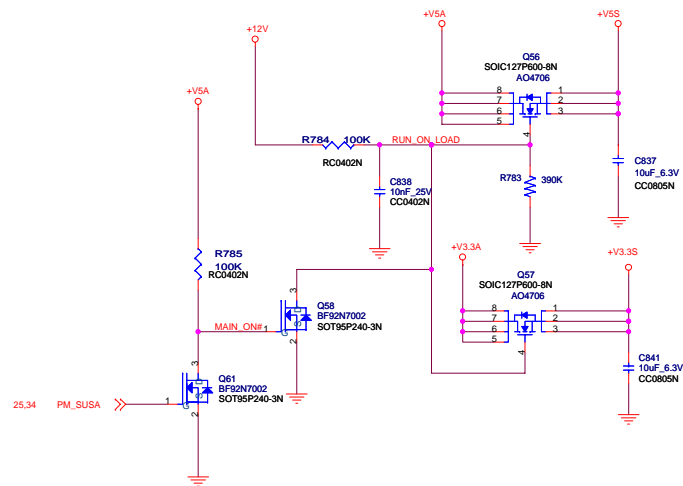


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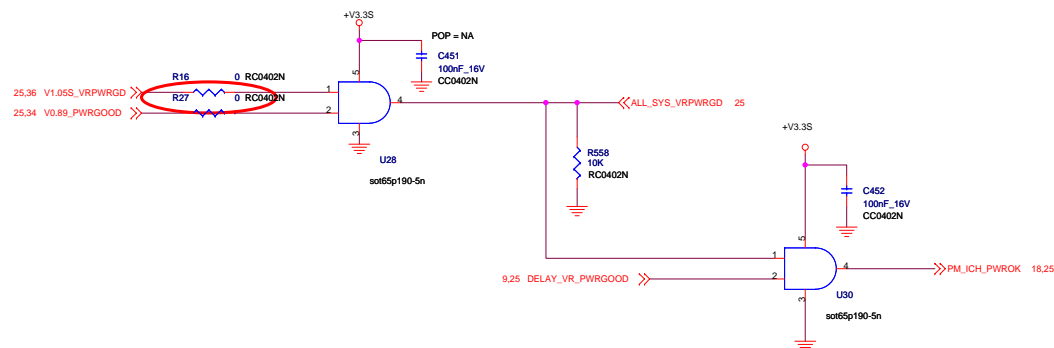


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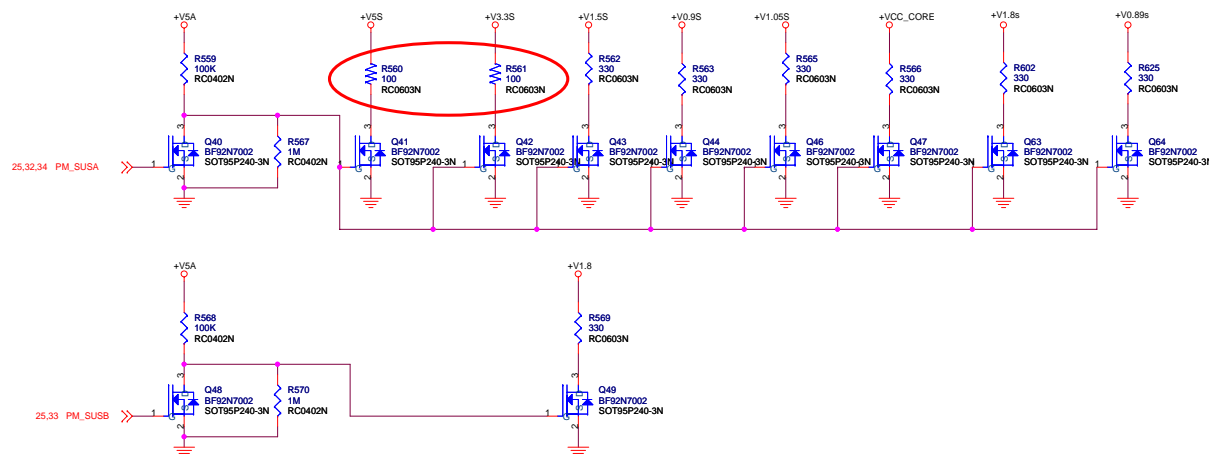
	BYD COMPANY LIMITED	
	Module Name	N10C
Page Name	POWER_+1.05V	Rev V0.1
Date: Tuesday, October 27, 2009	Sheet 36	of 42



Power Good



Power Discharge Circuit



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0619

- 1.Change Memory Slot footprint from SMD-CN-60X820P-200AN to SMD-CN-60X820P-200BN  
2.Change R155 R156 R88 R85 100Ohm 1206 to 00hm 0805

0623

- 1.Addition F10 fuse for Adaptor In  
2.Change F3 Fuse to PJ13 Jumper for system Power ;Change F4 Fuse to PJ14 Jumper for 1.8V Power;  
Change F5 Fuse to PJ15 Jumper for 1.05V Power

0624

- 1.Swap DM1&DM2;CN3 (SO-DIMM Slot) M\_A\_DM1 & M\_A\_DM2 Error  
2.Remove audio 、 3G from mainboard to small card

0629

- 1.Change tigerpoint GPIO1 to SMC\_RUNTIME SCI#,GPIO7 NC;  
Original GPIO1 link ICH\_GPIO1 pullup 8.2K to 3.3S,GPIO7 link SMC\_RUNTIME\_SCI#  
2.Remove R290---GPIO 14重复  
3.Change SMC\_EXTSMI# to GPIO10 and Pullup to 3.3A;  
Original SMC\_EXTSMI# link to GPIO8 and pullup to 3.3S

0630

- 1.修改RTS159外围电路: A3V3线删除, D3V3改为直连线, 删除R577、R578 (0 欧姆) ;  
删除C313(10PF) ; 45pin MODE\_SEL直接接地, 删除R591(0 欧姆) 和C470(47PF NA)

0701

- 1.LAN的连接器CN9 换为8PIN连接器  
2.3G的connector由64PIN换为40PIN  
3.EC :Remove R627 R364;D18 stuff;change R628,R363 to 0603--follow S102  
4.Addition R659,R673 for USB Common mode choke dual lay

0702

- 1.change R71,R73 to 0402 and add " HSYNC/VSYNC; LOCATE SERIES RESISTOR STRAPS (R71,R73) WITHIN 750 MILS OF MCH"

0706

- 1.Change R371 to 0402 P/N C2040-2551;R329 footprint ERROR ,change to 0402;  
Change R343 to 0402,P/N C2040-0491;Change R396 to 0402  
2.change U4 P/N to C2140-9SP1; change U5,U9,U28,U29,U30 P/N to C2140-9SN1

0707

- 1.Change Mini Card -WIFI footprint to SMD-CN-80X820P-52DN

0711

- 1.3G的connector信号标注页码

0714 (Change Power Solution)

- 1.将VCORE的第9PIN的电阻替换为附件中80.6K的电阻  
2.Remove CPU Thermal sensor Q45,Q53.U3 pin6 pullup;Remove SYS\_SHDN# net  
3.Remove KBC pin20 +V5SPOK net  
4.Addition KBC pin106 IBAT\_CH link to Charger,for control charge Current  
5.Addition KBC pin105 ALW\_PWRGD link to page32

0715

- 1.Modify Page37 power good schematic, Remove U29  
2.U39 封装改为SOIC127P600-9N  
3.L15封装改为L605P-660X660-2N  
4.Modify DataBase,Unbate R783 R560 R561 R562 R563 R565 R566 R569 R602 R625 to database  
5.Change L15 to 1.5uH,Original is 1.0uH  
6.Modify USB pair:6&0 , 7&3 , 1&4 for layout

0717

1. .+V1.8 MOSFET改为独立上下两颗AO4468和AO4706

0720

- 1.完善power map  
2.修改了Power On Sequence

0722

- 1.+5A&+3A预留0欧姆电阻R812给ALW\_PWRGD  
2.V\_CORE部分的IMVP\_VR\_ON信号加0欧姆电阻R699并预留接地电容0.1uF的C526。  
3.R594改为NA

0723

- 1.J3的封装改为: SMD-CN-100P-4BN

0724

- 1.修改电源部分电感

0725

- 1.R671与R672修改为0402封装

0727

- 1.Follow Mechanical DXF File

0803

- 1.Change R93 to 0402,Original 0805 --- See Page 10 Note  
2.Addition FB10 for Page 14 LVDS Power  
3.Addition FB23,FB24 and 0.1uF Cap for CRT HSYNC VSYNC  
4.Page 22 Reserve Cap for 3.3V & 1.5V  
5.Page 25 EC Pin 121 122 follow S101  
6.Addition VID Option for Power test

0807

- 1.Change Cardreader to GL827S  
2.Addition Spin WIFI接口for 算法部WIFI

0811

- 1.Change Thermal Sensor to W83L771AWG  
2.Change R77 to 665ohm;C219 to 0.01uF;R186 to 0ohm;change C161 to 0.1uF;change C167 to 0.01uF;add R193 R194,but NA

0812

- 1.change R231 to 0ohm;add PM\_RSMRST# to LAN\_RST#  
2. Follow DXF ,Rotate CN19 180度

0813

- 1.change ISL6268 to APW7138 R510 R516 C407 C420 C411 C418 NA  
2. CP8 CP9 change to 6mohm,取消NA; CP29 NA

0817

- 1.change X1 to R49SSA-014318-F20-YYY-YBA(YOKE);change X8 to R49SSA-025000-F20-YYY-YBA (YOKE); change Q8 Q9 Q11 Q12 to PDTCL44EU+115  
2. MCN3 MCN4 change to 34 Pin

0818

- 1.R683去掉NA, 改为上件

0820

- 1.MCN2换为与J17相同  
2. FAN/CAMRA/AUDIO/BT 增加测试点

0821

- 1.VCORE部分增加PWR\_MON信号接到KBC (NA); VCORE增加一个JUMP

0831

- 1.TP开关改为TME-533K-Q-T/R, 与J17相同

0928

- 1.PN6与PP6对换: R24 R25拿掉; POWER0.89与1.05V的上拉电改为+V3.3S

0929

- 1.wifi模块的USB信号线对换: 针对3G功能R617.R618上键, R106 R107换为0805封装

0930

- 1.KBC\_MUTE#信号线加二极管D23

1010

- 1.V\_CORE部分按POWER要求del R661 R662; change R666 to 1K 1%; change C688 to 1.5nF;del C826  
2.card reader pin 21 pull up 10K(R822) to CARD\_VDD

1015

- 1.算法部WIFI模块POWER改为3.3S, R614上键, R613改为NA  
2.R560 R561由330欧姆换为100欧姆

1017

- 1.移除热敏电阻部分  
2.change Q7,8,9,10,11,12,50 footprint sot65p210-3n  
3C182,C183,C187上件  
4.change C372 to 470nF  
5.remove 1.05Voutput cap CP29  
6.change 7107 to isl6236  
7.change R782 to 1.21K ,P/N:C2310-48Z1AX  
8.change R808 to 1.5K ,P/N:C2040-7951AX  
9.CN17 反转for ME  
10.change R57 to 4.7K,follow S102,C128 NA

1019

- 1.C822 C809改为NA;  
2.change R599 to 3.65K, P/N:10036268-00  
3.change C317 to C2300-5011AX,change R353 to C2040-2611AX


1020

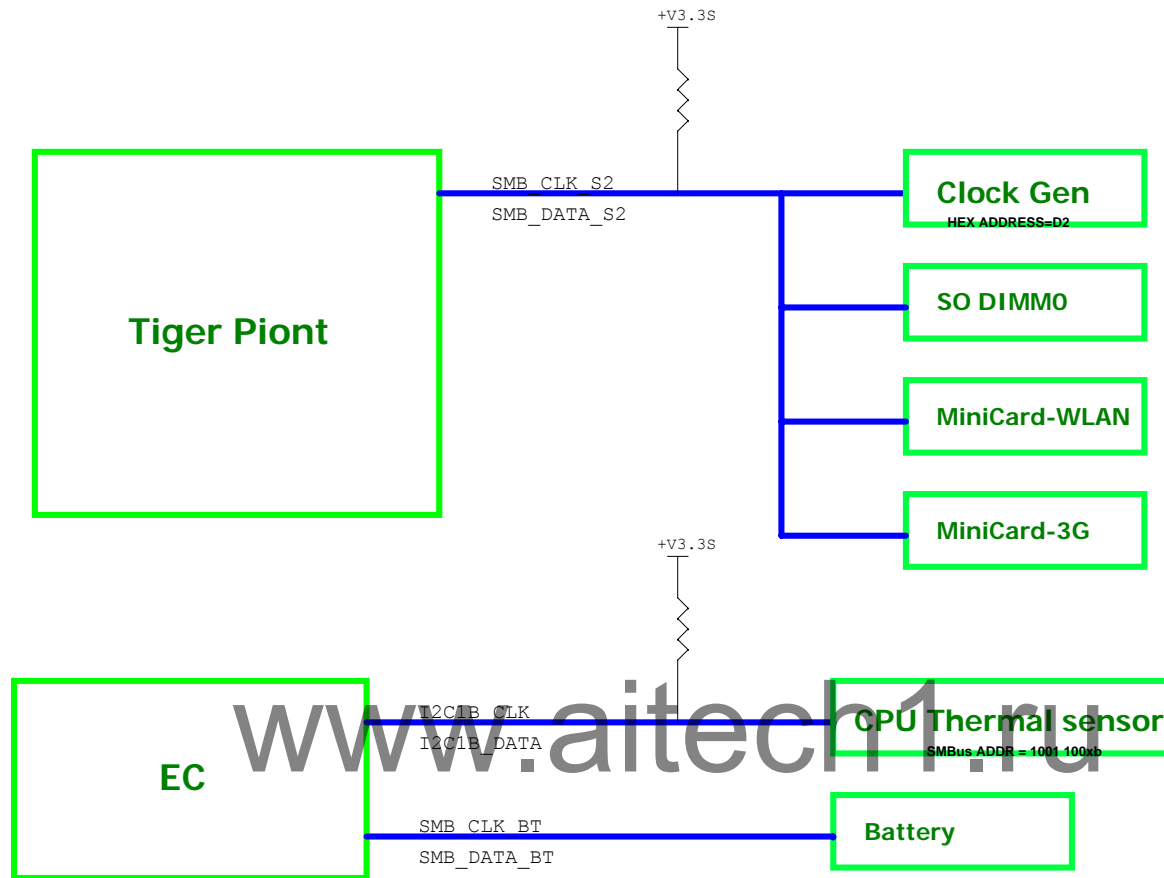
- 1.CRT部分 c608 c610 change to 47PF;FB5 FB6 FB7 change to 75欧/100MHz(暂无料号)

1021

- 1.FOLLOW EMC整改措施, 磁珠暂用0欧姆电阻代替  
2.CN11翻转  
2.CP27 CP28换为富士通电容 MCP1换为220uF

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RTM875N

CLK CPU BCLK CLK CPU BCLK# 166MHz  
CLK MCH BCLK CLK MCH BCLK# 166MHz  
DREFCLK DREFCLK# 96MHz  
DREFSSCLK DREFSSCLK# 100MHz  
CK PE 100M MCH DP CK PE 100M MCH DN 100MHz

Pineview

M\_CLK\_DDR0 M\_CLK\_DDR#0  
M\_CLK\_DDR1 M\_CLK\_DDR#1

SO DIMMO

CLK PCIE SATA CLK PCIE SATA# 100MHz  
CLK PCIE ICH CLK PCIE ICH# 100MHz  
CLK USB48 48MHz  
CLK PCIF ICH 33MHz  
CLK REF ICH 14MHz

Tiger Piont

ACZ BITCLK C 24MHz

Audio Codec

32.768KHz

48M CardReader 48MHz

RTS5159

12MHz OPTION

CLK PCIE LAN CLK PCIE LAN# 100MHz

LAN

25MHz

CLK PCI-4 33MHz

80 Port

CLK PCIE MINICARD1 CLK PCIE MINICARD1# 100MHz

MiniCard-WLAN

CLK PCIE CDMA SUB CLK PCIE CDMA# SUB 100MHz

MiniCard-3G


CLK KBCPCI 33MHz

EC(WINBOND775)

32.768KHz

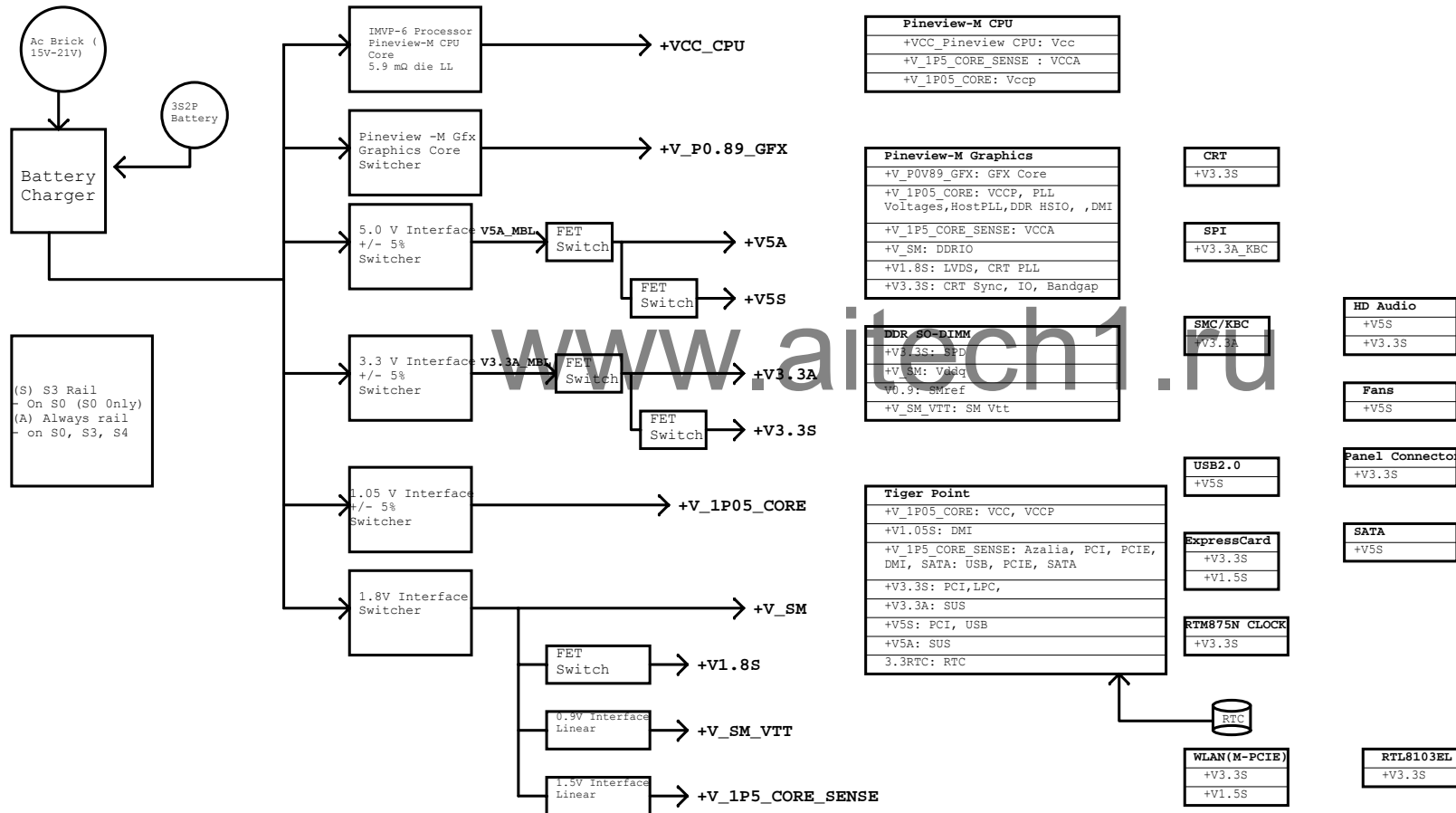
14.318MHz

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# Pine Trail-M Power Map



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GPIO TABLE FOR TIGER POINT			
NAME	POWER WELL	AFTER PLTRST	PIN NAMES
GPIO0	CORE	INPUT	SATA GP DETECT N
GPIO1	CORE	DRIVEN	SMC_RUNTIME_SCI#
GPIO2	CORE	HI-Z	INT_PIRQ#
GPIO3	CORE	HI-Z	INT_PIRQ#
GPIO4	CORE	HI-Z	INT_PIRQ#
GPIO5	CORE	HI-Z	INT_PIRQ#
GPIO6	CORE	INPUT	NOT ASSIGNED
GPIO7	CORE	INPUT	NOT ASSIGNED
GPIO8	RESUME	INPUT	NOT ASSIGNED
GPIO9	RESUME	INPUT	NOT ASSIGNED
GPIO10	RESUME	INPUT	SMC_EXTSMI#
GPIO11	RESUME	DRIVEN	SMBALERT- PULL UP
GPIO12	RESUME	INPUT	GPIO12- PULL UP
GPIO13	RESUME	INPUT	SMC_WAKE_SCI#
GPIO14	RESUME	INPUT	GPIO14 - PULL UP
GPIO15	RESUME	INPUT	GPIO15 - PULL UP
GPIO17	CORE	HIGH	STRAP2
GPIO22	CORE	DRIVEN	GPIO22 - PULL UP
GPIO23	CORE	HIGH	LDRQ1
GPIO25	RESUME	HIGH	GPIO25 - PULL DOWN
GPIO29	RESUME	INPUT	OC5#
GPIO30	RESUME	INPUT	OC6#
GPIO31	RESUME	INPUT	OC7#
GPIO36	CORE	INPUT	GPIO36 - PULL UP
GPIO48	CORE	HIGH	STRAP1
GPIO49	CORE	HI-Z	H_PWRGD

VOLTAGE RAILS						
POWER NAME	VOLTAGE	Tolerance	ACTIVE IN			
			S0	S3	S4	s5 adt mode
+V_DC_IN			Y	Y	Y	Y
+V5A	5V	5%	Y	Y	Y	Y
+V3.3A	3.3V	5%	Y	Y	Y	Y
+V1.8	1.8V	5%	Y	Y	N	N
+V5S	5V	5%	Y	N	N	N
+V3.3S	3.3V	5%	Y	N	N	N
LCD_VCC	3.3V	5%	Y	N	N	N
+V1.8S	1.8V	5%	Y	N	N	N
+V1.5S	1.5V	5%	Y	N	N	N
+V1.05S	1.05V	5%	Y	N	N	N
+VCC_CORE	0.8~1.175		Y	N	N	N
+V0.9S	0.9V	0.88~0.92	Y	N	N	N
+V0.89S	0.89V	0.85~0.93	Y	N	N	N

GPIO TABLE FOR KBC\_775

SIGNAL NAME	SCHEMATIC NET	PIN NUM
GPIO01/TB2	PM_SLP_S3#	64
GPIO03/AD6	PM_+V1.8S_EN#	95
GPIO04/AD5	SENSOR	96
GPIO05/AD4	PM_+V0.89S_EN	108
GPIO06	SMC_SHUTDOWN#	93
GPIO07/AD7	PM_SUSB	94
GPIO10/LPCPD	IMVP_VR_ON	124
GPIO11/CLKRUN	CHG	8
GPIO12/PSDAT3	DELAY_VR_PWRGOOD	13
GPIO13/C_PWM	TP23	62
GPIO14/TB1	NC	63
GPIO15/A_PWM	FAN_ON	32
GPIO16/CIRTX1	KBC_MUTE#	114
GPIO17/SCL1	I2C1B_CLK	70
GPIO20/TA2	NC	117
GPIO21/B_PWM	BL_ADJ	118
GPIO22/SDA1	I2C1B_DATA	69
GPIO23/SCL3	DDRVR_PWRGD	119
GPIO24/LDRQ	ICH_DRQ#0	6
GPIO25/PSCLK3	VR_PWRGOOD	12
GPIO26/PSCLK2	TP26	10
GPIO27/PSDAT2	TP25	11
GPIO30/CIRTX2	WIRELESS_ON	109
GPIO31/SDA3	V1.05S_VRPWRGD	120
GPIO32/D_PWM	TP24	65
GPIO33/H_PWM	PWR_LED_RED	66
GPIO34/CIRRXL	CAM_ON	14
GPIO35/PSDAT1	TBDATA	71
GPIO36/TB3	PM_SLP_S4#	15
GPIO37/PSCLK1	TBCLK	72
GPIO40/F_PWM	NUMLOCK_LED#	16
GPIO41	ADP_IN#	80
GPIO42/TCK	PM_+V1.05S_EN	17
GPIO43/TMS	PM_SUSB	20

GPIO TABLE FOR KBC\_775

SIGNAL NAME	SCHEMATIC NET	PIN NUM
GPIO44/RDY	WIFI_ON#	21
GPIO45/F_PWM	GPIO45/F_PWM	GPIO45/F_PWM
GPIO46/CIRRXM/TRST	WIFI_LED	23
GPIO47/SCL4	BLCTL	24
GPIO50/TD0	BT_LED	25
GPIO51/TA3	ALL_SYS_VRPWRGD	26
GPIO52/CIRTX2/RDY	TP20	27
GPIO53/SDA4	L_BKLKEN	28
ECSCI/GPIO54	SMC_RUNTIME_SCI#	29
GPIO55/CLKOUT	LID#	30
GPIO56/TA1	FAN_SPEED1	31
KBSOUT17/GPIO57	PM_PWRBTN#	33
KBSOUT16/GPIO60	PM_RSMRST#	34
GPIO65/SMI	SMC_EXTSMI#	9
GPIO66/G_PWM	PWR_LED_GRN	81
GPIO67/PWUREQ	SMC_WAKE_SCI#	123
GPIO70/IRRX2_IRSL0	BT_ON	73
GPIO71/IRTX/SOUT2	PM_ICH_PWR0K	74
GPIO72/IRRX1/SIN2	V1.5_POWGD	75
GPIO73/SCL2	SMB_CLK_BT	67
GPIO74/SDA2	SMB_DATA_BT	68
GPIO75/SPI_CLK	NC	82
GPIO77/SPI_DI	PWR_MON	84
GPIO81	SMC_ONOFF#	91
GPIO87/CIRRXM/SIN	3G_ON_A	113
GPIO90/AD0	THERM_BAT	97
GPIO92/AD2	I_ADP	99
GPIO94/DA0	BAT-DEK#	101
GPIO95/DA1	ALW_PWRGD	105
GPIO96/DA2	IBAT_CH	106
GPIO97/DA3	BAT_CH	107

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